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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/646,728	08/25/2003	Yuichiro Iguchi	360842006210	1179
7	7590 03/31/2004		EXAMINER	
Barry E. Bretschneider			QUARTERMAN, KEVIN J	
Morrison & Fo	erster LLP			
Suite 300			ART UNIT	PAPER NUMBER
1650 Tysons Boulevard			2879	
McLean, VA 22102			DATE MAILED: 03/31/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/646,728	IGUCHI ET AL.			
		Examiner	Art Unit			
<u> </u>		Kevin Quarterman	2879			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1)⊠	1) Responsive to communication(s) filed on <u>25 August 2003</u> .					
2a) <u></u>	his action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	ion of Claims					
	4)⊠ Claim(s) <u>5-10</u> is/are pending in the application.					
•	4a) Of the above claim(s) is/are withdrawn from consideration.					
	5) Claim(s) is/are allowed.					
	☐ Claim(s)is/are allowed. ☐ Claim(s) <u>5-10</u> is/are rejected. ☐ Claim(s) is/are objected to.					
·	Claim(s) is/are objected to.  Claim(s) are subject to restriction and/or election requirement.					
	on Papers	<b>1</b>				
	•					
9) The specification is objected to by the Examiner.						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority ι	under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:						
	1. Certified copies of the priority documents have been received.					
	2. Certified copies of the priority documents have been received in Application No					
	3. Copies of the certified copies of the prior		ed in this National Stage			
application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.						
- 8	see the attached detailed Office action for a list	of the certified copies not receive	d.			
Attoob	We)					
Attachment(s)						
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)	4) Linterview Summary Paper No(s)/Mail Da	(P1O-413) ite			
3) 🛛 Inforr	nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 0825.		atent Application (PTO-152)			

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#### **DETAILED ACTION**

## **Priority**

1. The statement following the title of the invention reading "This application is a continuation of 09/600,703, filed July 21, 2000" should also be followed by the current status of the nonprovisional parent application referenced. In the instant case, application 09/600,703 is now U.S. Patent No. 6,635,992. Appropriate correction required.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 5-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Komaki (US 6236160).
- 4. Regarding independent claim 5, Figure 7 of Komaki shows a board for a plasma color display on which striped barrier ribs (12) for partitioning address electrodes (X, Y) and discharge spaces (Gp) are formed and on which phosphor layer stripes (10) emitting red, green, and blue light are formed in grooves between the respectively adjacent barrier ribs, wherein two or more phosphor layer stripes emitting light of the same color are formed in respectively adjacent grooves.

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5. Regarding claim 6, Figure 7 of Komaki shows two or more blue light emitting phosphor layer stripes (10B) are formed in the respectively adjacent grooves.

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- 6. Regarding claim 7, Figure 7 of Komaki shows a plasma display comprising a front glass board (1) having electrodes (X, Y), a dielectric (5) and a protective film (6) formed on the front glass board and a rear glass board (2) having electrodes (7), a dielectric (8), barrier ribs (12) and phosphors (10) formed on the rear glass board, the board for a plasma display according to claim 5 being used as the rear board.
- 7. Regarding claim 8, Komaki discloses a process for producing the board for a plasma display according to claim 5 comprising applying a photosensitive paste over a surface of the board, exposing the photosensitive paste to a barrier rib pattern, developing the exposed photosensitive paste of the board to remove portion dissolved by the developer, and firing the developed board at 450°C to 620°C so as to form barrier ribs on the board (col. 5, In. 24-67; col. 6, In. 1-48).
- 8. Regarding claim 9, Figure 7 of Komaki shows a plasma display comprising a front glass board (1) having electrodes (X, Y), a dielectric layer (5) and a protective film (6) formed on the front glass board and a rear glass board (2) having electrodes (7), a dielectric (8), barrier ribs (12) and phosphors (10) formed on the rear glass board, the board for a plasma display according to claim 6 being used as the rear board.
- 9. Regarding claim 10, Komaki discloses a process for producing the board for a plasma display according to claim 6 comprising applying a photosensitive paste over a surface of the board, exposing the photosensitive paste to a barrier rib pattern, developing the exposed photosensitive paste of the board to remove portion dissolved

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by a developer, and firing the developed board at 450°C to 620°C so as to form barrier ribs on the board (col. 5, In. 24-67; col. 6, In. 1-48).

### **Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Quarterman whose telephone number is (571) 272-2461. The examiner can normally be reached on M-TH (7-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin Quarterman Examiner Art Unit 2879

20 March 2004

Nimesh Patel Supervisory Patent Examiner Art Unit 2879

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